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SILICON-ON-INSULATOR TECHNOLOGY: Materials To VLSI

materials, some techniques have been developed especially to assess the SOI crystal quality and its interface properties, and to measure the film thickness and the lifetime in Silicon-on-Insulator materials.

Silicon-on-Insulator Technology: Materials to VLSI

Silicon-on-insulator material can also be produced from a bulk silicon wafer by isolating a thin silicon layer from the substrate through the formation and oxidation of porous silicon (FIPOS) or

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Silicon on Insulator - Wikipedia

Silicon on insulator (SOI) technology refers to the use of a layered silicon insulator silicon substrate in place of conventional silicon substrates in semiconductor manufacturing, especially microelectronics, to reduce parasitic device capacitance, thereby improving performance.

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Describes the different facets of Silicon-on-Insulator (SOI) technology. This title reviews SOI materials, devices and circuits. It discusses SOI fabrication and characterization techniques, SOI device processing, the physics of the SOI MOSFET as well as that of SOI other devices, and the performances of SOI circuits.